

# PATENT ABSTRACTS OF JAPAN

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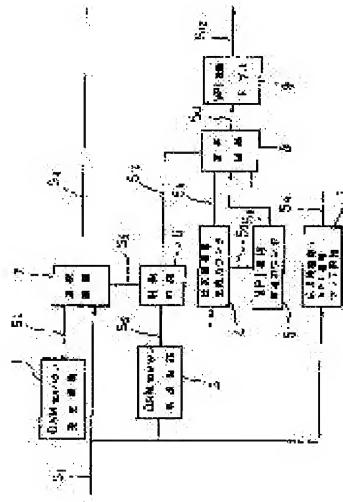
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## (54) ALARM TRANSFER SYSTEM IN ATM SYSTEM

### (57)Abstract:

**PURPOSE:** To prevent a delay by an access time without provision of a memory for VPI registration by sending a transmission line number counted by a transmission line number generating counter and a VPI number counted by a VPI number generating counter as VPI information when a fault occurrence of a transmission line is detected.

**CONSTITUTION:** A transmission line number generating counter 4 counts all number of transmission lines for each signal input speed and outputs transmission line numbers S6, S7 in response to a fault detection signal S5. A VIP (Virtual Path Identifier) number generating counter 5 counts all VPIS in the transmission line and outputs a VPI number S8 in response to the transmission line number S7. On the occurrence of a fault of the transmission line, a 1st selection circuit 7 selects an OAM cell S3 generated by an OAM cell pattern generating circuit 1 to be sent and VPI information (transmission line number S6 and VPI number S8) in the case of fault occurrence is sent to a VPI conversion table 9 by a 2nd selection circuit 8. Thus, alarm transfer is attained without need of a memory for the VPI registration table and causing a delay by the access time.




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## LEGAL STATUS

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